- 173. The combination of claim 174 wherein said equilibration switch includes a plurality of transistors, and wherein said transistors are rendered conductive with an equalization control signal.
- 174. A method of controlling the conduction of a pair of isolation transistors in a sense amplifier responsive to an array, comprising the steps of:

rendering the pair of transistors conductive with a control signal that is a boosted version of the voltage used by the array; and

rendering the pair of transistors nonconfluctive by removing said control signal.

175. The method of claim 174 wherein said step of rendering the pair of transistors conductive includes the step of rendering the transistors conductive with a control signal that is approximately a Vt higher than the voltage used by the array. --

## **REMARKS**

The instant amendment presents new claims 81 - 175 for examination. No new matter has been added by claims 81 - 175.

## STATEMENT REQUESTING DELETION OF INVENTORS

As a result of the restriction requirement, the following inventors named in the parent application are not inventors of the invention claimed in the instant divisional application:

Raymond J. Beffa Frank K. Ross Larry D. Kinsman Ronald L. Taylor John S. Mullin

Please file the instant application in the names of the remaining inventors (Keeth, Derner and Bunker) in accordance with 37 CFR 1.63 (d).

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DO THE PLAN